

产品硬件设计及装配 - 错误 #3644

evmt4板卡出现启动时DDR初始化时失败，需要硬件协助定位。

2025-07-01 10:46 - 战 弋戈

状态:	新建	开始日期:	2025-07-01
优先级:	普通	计划完成日期:	
指派给:		% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:		耗时:	0.00 小时
描述			
U-Boot 2018.11-rc2 (Jun 04 2025 - 15:24:39 +0800)			
Clock Configuration: CPU A72: 1800 MHz APE: 1400 MHz Bus(PLL2): 1000 MHz SDIO/GMAC: 1000 MHz Model: Smartlogic ucp4008 EVB DRAM: UCP4008 ddr4 reset ddr4_init start ddr4_init 3200 MT/Mbps start--- Info: Polling UctWriteProtShadow, bit0 ... Right sequence: 1: Init device 2: Read enable 3: Fine write leveling 4: Read dq deskew 5: MPR read delay center 6: Write leveling coarse 7: Write delay center 8: Read delay center 9: Max read latency PMU Major Msg: FATAL ERROR ! Info: Mail Box = ff Error: Firmware training failed. freq == 0x96 可能的原因：虚焊			