

基站横联 - 错误 #3829

横联slot8/9，PDC解不对

2025-07-29 12:40 - 高峰

状态:	已解决	开始日期:	2025-07-29
优先级:	普通	计划完成日期:	
指派给:	张倩	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:		耗时:	0.00 小时
描述			

历史记录

#1 - 2025-07-29 12:40 - 高峰

slot8/9 PDC采数发现没有数据
需要deofdm 采数分析

#2 - 2025-08-01 14:57 - 张倩

思朗基线发端平台版本用的是伪常发（7d2u），所以收端slot8 9没收到数；修改deofdm流程并替换收发统一的平台版本后，收端有数，除slot8外 pdc可以解对；

#3 - 2025-08-01 17:52 - 张倩

- 文件 7898a503-d409-4996-bc8f-e86f8e0b0065.jpeg 已添加
- 文件 img_v3_02oo_f97f45bf-0bab-4c4d-b926-ddeadee281ag.jpg 已添加
- 文件 img_v3_02oo_15c35492-2864-47b1-a217-bfe10c1ca2cg.jpg 已添加

slot8参数错误：加打印发现l1c并不是读的提前两个slot配的rxconfig消息，没有按照slot存，而是直接读的当前slot的rxcfg存到cellcfg中；

```
[ERR][L1C]TargetSlot[1] curSlot[2] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[4] curSlot[3] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[2] curSlot[3] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[5] curSlot[4] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[3] curSlot[4] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[6] curSlot[5] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[4] curSlot[5] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[7] curSlot[6] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[5] curSlot[6] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[8] curSlot[7] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[6] curSlot[7] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[9] curSlot[8] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[7] curSlot[8] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[10] curSlot[9] Coreset_ind[1] IsDynamicSlot[0]
[ERR][L1C]TargetSlot[8] curSlot[9] Coreset_ind[1] HL_Dynamic_slot[0]
[ERR][L2]targetSlot[11] curSlot[10] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[9] curSlot[10] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[12] curSlot[11] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[10] curSlot[11] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[13] curSlot[12] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[11] curSlot[12] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[14] curSlot[13] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[12] curSlot[13] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[15] curSlot[14] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[13] curSlot[14] Coreset_ind[0] HL_Dynamic_slot[1]
[ERR][L2]targetSlot[16] curSlot[15] Coreset_ind[0] IsDynamicSlot[1]
[ERR][L1C]TargetSlot[14] curSlot[15] Coreset_ind[0] HL_Dynamic_slot[1]
```

```

}
break;

case RX_CONFIG_REQ:
{
    Rx_config_req_t * HL_rx_cfg_p = (Rx_config_req_t *) (msg_general_hdr + 1);
    uint16_t targetSlot = (HL_rx_cfg_p->Slot) % 20;
    DD_SLOT_CFG * dd_cfg_p;
    PHYStateDLRx * psPHYStateRx = phy_gue_dl_get_ctx();
    cellSearchRxStruct * pCellSearchRxStruct = &psPHYStateRx->sCellSearchRxStruct;
    //dd_cfg_p = &ueCfg->dd_cfg[targetSlot];
    if(HL_rx_cfg_p->IsDynamicSlot == 0)
    {
        STORE_EX_B(&pCellSearchRxStruct->L1C_cs_start_flag, TRUE);
    }
    STORE_EX_B(&(dd_cfg_p->HL_Dynamic_slot), HL_rx_cfg_p->IsDynamicSlot);
    //STORE_EX_S(&(cellCfg->PNID), (uint16_t)HL_rx_cfg_p->Pnid);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.Pcch_symbol_num), HL_rx_cfg_p->Pcch_symbol_num);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.Pcch_agg_level), HL_rx_cfg_p->Pcch_agg_level);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.Pcch_start_rb), HL_rx_cfg_p->Pcch_start_rb);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.Pcch_rb_num), HL_rx_cfg_p->Pcch_rb_num);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.Coreset_ind), HL_rx_cfg_p->Coreset_ind);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.regBundleSize), HL_rx_cfg_p->regBundleSize);
    STORE_EX_B(&(cellCfg->HL_PcchCfg.interleaverSize), HL_rx_cfg_p->interleaverSize);
    STORE_EX_S(&(cellCfg->HL_PcchCfg.shiftIndex), HL_rx_cfg_p->shiftIndex);
    STORE_EX_S(&(cellCfg->prev_Fss), HL_rx_cfg_p->Previous_SSBFreqIdx);
    STORE_EX_S(&(cellCfg->HL_dyn_P_delay), HL_rx_cfg_p->Pdelay);

    SteDedPdcchInfo *pdcchInfo;
    SteCoreSet *coreSet;
    SteSearchSpace *ss;
    pdcchInfo = &ueCfg->d1Cfg.d1BwpDed[0].pdcchInfo ; /*Dynamic Slot*/
    STORE_EX_B(&pdcchInfo->numCS, 1);
    coreSet = &pdcchInfo->coreSet[0];
}

```

【解决方案】按照targetslot存参数

```

// case,
case RX_CONFIG_REQ:
{
    Rx_config_req_t * HL_rx_cfg_p = (Rx_config_req_t *) (msg_general_hdr + 1);
    uint16_t targetSlot = (HL_rx_cfg_p->Slot) % 20;
    DD_SLOT_CFG * dd_cfg_p;
    PHYStateDLRx * psPHYStateRx = phy_gue_dl_get_ctx();
    cellSearchRxStruct * pCellSearchRxStruct = &psPHYStateRx->sCellSearchRxStruct;
    dd_cfg_p = &ueCfg->dd_cfg[targetSlot];
    if(HL_rx_cfg_p->IsDynamicSlot == 0)
    {
        STORE_EX_B(&pCellSearchRxStruct->L1C_cs_start_flag, TRUE);
    }
    STORE_EX_B(&(dd_cfg_p->HL_Dynamic_slot), HL_rx_cfg_p->IsDynamicSlot);
    //STORE_EX_S(&(cellCfg->PNID), (uint16_t)HL_rx_cfg_p->Pnid);
    STORE_EX_B(&(dd_cfg_p->Pcch_symbol_num), HL_rx_cfg_p->Pcch_symbol_num);
    STORE_EX_B(&(dd_cfg_p->Pcch_agg_level), HL_rx_cfg_p->Pcch_agg_level);
    STORE_EX_B(&(dd_cfg_p->Pcch_start_rb), HL_rx_cfg_p->Pcch_start_rb);
    STORE_EX_B(&(dd_cfg_p->Pcch_rb_num), HL_rx_cfg_p->Pcch_rb_num);
    STORE_EX_B(&(dd_cfg_p->Coreset_ind), HL_rx_cfg_p->Coreset_ind);
    STORE_EX_B(&(dd_cfg_p->regBundleSize), HL_rx_cfg_p->regBundleSize);
    STORE_EX_B(&(dd_cfg_p->interleaverSize), HL_rx_cfg_p->interleaverSize);
    STORE_EX_S(&(dd_cfg_p->shiftIndex), HL_rx_cfg_p->shiftIndex);
    STORE_EX_S(&(dd_cfg_p->prev_Fss), HL_rx_cfg_p->Previous_SSBFreqIdx);
    STORE_EX_S(&(dd_cfg_p->HL_dyn_P_delay), HL_rx_cfg_p->Pdelay);

    SteDedPdcchInfo *pdcchInfo;
    SteCoreSet *coreSet;
    SteSearchSpace *ss;
    pdcchInfo = &ueCfg->d1Cfg.d1BwpDed[0].pdcchInfo ; /*Dynamic Slot*/
    STORE_EX_B(&pdcchInfo->numCS, 1);
    coreSet = &pdcchInfo->coreSet[0];
    STORE_EX_B(&coreSet->csId, 0);
    STORE_EX_B(&coreSet->numCce, HL_rx_cfg_p->Pcch_agg_level * 2);
}

```

#4 - 2025-08-01 17:52 - 张倩

- 状态从 新建 变更为 已解决

文件

7898a503-d409-4996-bc8f-e86f8e0b0065.jpeg	372 KB	2025-08-01	张倩
img_v3_02oo_f97f45bf-0bab-4c4d-b926-ddeadee281ag.jpg	205 KB	2025-08-01	张倩
img_v3_02oo_15c35492-2864-47b1-a217-bfe10c1ca2cg.jpg	201 KB	2025-08-01	张倩