

3.0基站产品测试 - 错误 #4590

ICIC特性，高干扰边缘ue调度在最后30个rb时，中心ue期望结果是4,209，现在是4,205，少了4个rb

2025-12-12 09:46 - 王 旭初

状态:	新建	开始日期:	2025-12-12
优先级:	一般	计划完成日期:	
指派给:	周 立伟	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:		耗时:	0.00 小时
问题归属:	DU	目标解决问题版本:	Rel_3.1.4
发现问题版本:	Rel_3.1.3		
描述			
<div>Line 2956: [2395177] [2025-12-01T01:46:35.636] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17021] startRB[249] numRB[4] raType[1] icicEnable[2] icicState[3] Line 3023: [2395227] [2025-12-01T01:46:35.638] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[18] numRB[4] raType[1] icicEnable[2] icicState[0] Line 3102: [2395301] [2025-12-01T01:46:35.646] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3127: [2395331] [2025-12-01T01:46:35.646] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3152: [2395341] [2025-12-01T01:46:35.647] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3201: [2395373] [2025-12-01T01:46:35.647] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[18] numRB[193] raType[1] icicEnable[2] icicState[0] Line 3226: [2395393] [2025-12-01T01:46:35.648] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3255: [2395417] [2025-12-01T01:46:35.649] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3280: [2395437] [2025-12-01T01:46:35.650] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3309: [2395461] [2025-12-01T01:46:35.650] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3334: [2395481] [2025-12-01T01:46:35.652] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3359: [2395501] [2025-12-01T01:46:35.652] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3403: [2395540] [2025-12-01T01:46:35.653] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3447: [2395579] [2025-12-01T01:46:35.654] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3472: [2395599] [2025-12-01T01:46:35.655] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3508: [2395633] [2025-12-01T01:46:35.655] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3560: [2395684] [2025-12-01T01:46:35.657] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17021] startRB[249] numRB[4] raType[1] icicEnable[2] icicState[3] Line 3583: [2395702] [2025-12-01T01:46:35.657] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[201] raType[1] icicEnable[2] icicState[0] Line 3632: [2395734] [2025-12-01T01:46:35.657] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[18] numRB[193] raType[1] icicEnable[2] icicState[0] Line 3666: [2395763] [2025-12-01T01:46:35.658] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3709: [2395801] [2025-12-01T01:46:35.659] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3734: [2395821] [2025-12-01T01:46:35.660] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3771: [2395853] [2025-12-01T01:46:35.660] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3830: [2395907] [2025-12-01T01:46:35.662] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[203] raType[1] icicEnable[2] icicState[0] Line 3855: [2395927] [2025-12-01T01:46:35.662] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3895: [2395956] [2025-12-01T01:46:35.663] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3941: [2396003] [2025-12-01T01:46:35.664] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 3966: [2396023] [2025-12-01T01:46:35.665] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 4000: [2396052] [2025-12-01T01:46:35.665] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[4] numRB[205] raType[1] icicEnable[2] icicState[0] Line 4047: [2396094] [2025-12-01T01:46:35.667] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[18] numRB[193] raType[1] icicEnable[2] icicState[0] Line 4096: [2396126] [2025-12-01T01:46:35.667] [YS_DL] [t:540902865776] [rg_sch_pusch_mgr.c:851] [DBG] [CORE-4] rgSCHCmnUlRbAllocForUe:Allocotr response for CellId[1] UE[17023] startRB[18] numRB[193] raType[1] icicEnable[2] icicState[0]</div>			
du已定位，提单跟踪			

历史记录

#1 - 2025-12-12 09:48 - 王 旭初

- 主题 从 高干扰边缘ue调度在最后30个rb时，中心ue期望结果是4,209，现在是4,205，少了4个rb 变更为
ICIC特性，高干扰边缘ue调度在最后30个rb时，中心ue期望结果是4,209，现在是4,205，少了4个rb

文件

后30个rb.jpg	1.06 MB	2025-12-12	王 旭初
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