

3.0基站产品测试 - 错误 #4754

[3.0产品测试]3.1.4pre1T4 , 军特26db 4T4R整机与底板的rx_gain补偿值填写错误

2026-01-19 15:04 - 刘 抒放

状态:	转测试	开始日期:	2026-01-19
优先级:	一般	计划完成日期:	
指派给:	刘 抒放	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:		耗时:	0.00 小时
问题归属:	DRV	目标解决问题版本:	Rel_3.1.4
发现问题版本:	Rel_3.1.4		

描述

定标值是4 , 没有数据的功放也应当填4.填写有误



```
"3336EB4A": {
    "pa_max_power": 0,
    "manufacturer": "JT",
    "freq": "N78(3.3-3.6GHZ)",
    "channel": 15,
    "pa_gain": 0,
    "pa_tune_low": 0,
    "pa_tune_high": 0,
    "min_temp": 0,
    "max_temp": 0,
    "gridVoltage_enable": 0,
    "gridVoltage_close": 0,
    "finalStageCurrent_static": 0,
    "finalStageCurrent_full": 0,
    "extreme_output_power": 0,
    "extreme_temp": 0,
    "extreme_reflect_power": 0,
    "rx_gain": 28800,
    "RXcalpara": {
        "ch1": 0,
        "ch2": 0,
        "ch3": 0,
        "ch4": 0
    }
},  
"3336EB4A": {
    "pa_max_power": 0,
    "manufacturer": "JT",
    "freq": "N78(3.3-3.6GHZ)",
    "channel": 15,
    "pa_gain": 43000,
    "pa_tune_low": 0,
    "pa_tune_high": 0,
    "min_temp": -40,
    "max_temp": 100,
    "gridVoltage_enable": 0,
    "gridVoltage_close": 0,
    "finalStageCurrent_static": 0,
    "finalStageCurrent_full": 0,
    "extreme_output_power": 45000,
    "extreme_temp": 100,
    "extreme_reflect_power": 30000,
    "extreme_voltage_low": 36000,
    "extreme_voltage_high": 50000,
    "rx_gain": 0,
    "RXcalpara": {
        "ch1": 255,
        "ch2": 255,
        "ch3": 255,
        "ch4": 255
    }
}
```

```

},
"3336EB4A": {
  "pa_max_power": 0,
  "manufacturer": "JT",
  "freq": "N78(3.3-3.6GHZ)",
  "channel": 15,
  "pa_gain": 43000,
  "pa_tune_low": 0,
  "pa_tune_high": 0,
  "min_temp": -40,
  "max_temp": 100,
  "gridVoltage_enable": 0,
  "gridVoltage_close": 0,
  "finalStageCurrent_static": 0,
  "finalStageCurrent_full": 0,
  "extreme_output_power": 45000,
  "extreme_temp": 100,
  "extreme_reflect_power": 30000,
  "extreme_voltage_low": 36000,
  "extreme_voltage_high": 50000,
  "rx_gain": 0,
  "RXcalpara": {
    "ch1": 255,
    "ch2": 255,
    "ch3": 255,
    "ch4": 255
  }
}

{
  "board": {
    "pa_max_power": 0,
    "manufacturer": "null",
    "freq": "null",
    "channel": 15,
    "pa_gain": 0,
    "pa_tune_low": 0,
    "pa_tune_high": 0,
    "min_temp": -40,
    "max_temp": 100,
    "gridVoltage_enable": 0,
    "gridVoltage_close": 0,
    "finalStageCurrent_static": 0,
    "finalStageCurrent_full": 0,
    "extreme_output_power": 0,
    "extreme_temp": 0,
    "extreme_reflect_power": 0,
    "extreme_voltage_low": 0,
    "extreme_voltage_high": 0,
    "rx_gain": 0,
    "RXcalpara": {
      "ch1": 255,
      "ch2": 255,
      "ch3": 255,
      "ch4": 255
    }
  }
}

```

历史记录

#1 - 2026-01-21 16:26 - 李明新

- 状态从 新建 变更为 进行中

已修改数据，待版本合入

#2 - 2026-01-29 15:53 - 李明新

- 状态从 进行中 变更为 审视

功能已完成，已合入3.2.1Pre1T1版本

#3 - 2026-01-29 15:53 - 李明新

- 状态从 审视 变更为 转测试

- 指派给 从 李明新 变更为 刘抒放

文件

mergepictures.net-merged-1768804386..png	204 KB	2026-01-19	刘抒放
mergepictures.net-merged-1768803359..png	221 KB	2026-01-19	刘抒放