

## B5G\_UE - 错误 #5234

### DD模式下，三终端帧号0的时隙0-5出现HARQ反馈为DTX的情况

2026-04-30 11:33 - 刘播雨

状态:	审视	开始日期:	2026-04-30
优先级:	一般	计划完成日期:	
指派给:	李常	% 完成:	0%
类别:		预期时间:	0.00 小时
目标版本:		耗时:	0.00 小时
<b>描述</b>			
【问题描述】：DD模式下，三终端帧号0的时隙0-5出现HARQ反馈为DTX的情况			

#### 历史记录

#1 - 2026-04-30 11:37 - 李常

- 状态从新建变更为进行中

#2 - 2026-04-30 11:42 - 刘播雨

【问题分析】：1、在终端帧号为0，时隙号0-4时，(dlSchedTm.sfn \* cmGCb.macSchCb->numOfSloerRadioFrame) + dlSchedTm.slot + loop小于k1值（5）时，发送PDSCH的帧号和时隙号计算错误，导致在帧号为0，时隙号0-4在配置时隙类型时出现错误，配置为了全发模式ENUM\_DD\_OM\_MDSU，应该配置为ENUM\_DD\_OM\_MASTER；  
2、在终端帧号为0，时隙号为5时，(dlSchedTm.sfn \* cmGCb.macSchCb->numOfSloerRadioFrame) + dlSchedTm.slot + loop等于k1值（5）时，发送PDSCH的帧号和时隙号计算错误，出现帧号为0，时隙号为5时，时隙类型配置错误，配置为了ENUM\_DD\_SLAVE，应该配置为ENUM\_DD\_OM\_SDMU全收模式；  
【解决方案】：修改(dlSchedTm.sfn \* cmGCb.macSchCb->numOfSloerRadioFrame) + dlSchedTm.slot + loop小于k1值（5）和等于k1值时的计算方法；

#3 - 2026-04-30 11:50 - 刘播雨

- 状态从进行中变更为审视

- 指派给从刘播雨变更为李常

【20260430】：问题已修改并验证通过，合入trunk版本，commit id号为：09f22ac9f818b2b03dbb6b7f7da7060970fdeb98

log路径：D:\lby\log\02\_dd\20260429\toRenzheng

出问题时log如下：

问题1：

```
[2026-04-29 09:37:42.296820[INFO]] src/l2/mac/csrr/wPsMacSch.c+6671 | wnSend25SlotType | dlSchedTm.sfn:[1023],dlSchedTm.slot:[10],cmGCb.airNetStatus:40,cmGCb.numDevice3
[2026-04-29 09:37:42.296829[INFO]] src/l2/mac/csrr/wPsMacSch.c+6672 | wnSend25SlotType | u8CurBcTrbIndex:[31],u8CurIndex:[0],u8LocalIndex:[1]
[2026-04-29 09:37:42.296835[DEBUG]] src/l2/mac/csrr/wPsMacSch.c+6681 | wnSend25SlotType | the index of current device in the cmGCb.deviceIdLst:[0]
[2026-04-29 09:37:42.296843[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop0,slotIdx0,slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296850[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop1,slotIdx1,slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296857[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop2,slotIdx2,slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296864[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop3,slotIdx3,slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296871[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop4,slotIdx4,slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296878[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop5,slotIdx5,slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296886[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop6,slotIdx6,slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296893[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop7,slotIdx7,slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296899[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop8,slotIdx8,slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.296906[INFO]] src/l2/mac/csrr/wPsMacSch.c+6864 | wnSend25SlotType | loop9,slotIdx9,slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.297321[DEBUG]] src/l2/clms/csrr/ys_fapi_task.c+355 | ysDIIm_FAPI_FillIDISubfrnMsg | msg_transfer_send_msg success,C_PLANE
[2026-04-29 09:37:42.297333[DEBUG]] src/l2/clms/csrr/ys_fapi_task.c+513 | ysDIIm_FAPI_FillIDICell1Msg | FAPI_msg,U[0xc2]
[2026-04-29 09:37:42.297355[DEBUG]] src/l2/clms/csrr/ys_fapi_task.c+554 | ysDIIm_FAPI_FillIDICell1Msg | msg_transfer_send_msg success,C_PLANE
[2026-04-29 09:37:42.301817[INFO]] src/l2/mac/csrr/wPsMacSch.c+6671 | wnSend25SlotType | dlSchedTm.sfn:[0],dlSchedTm.slot:[0],cmGCb.airNetStatus:40,cmGCb.numDevice3
[2026-04-29 09:37:42.301826[INFO]] src/l2/mac/csrr/wPsMacSch.c+6672 | wnSend25SlotType | u8CurBcTrbIndex:[0],u8CurIndex:[0],u8LocalIndex:[1]
[2026-04-29 09:37:42.301832[DEBUG]] src/l2/mac/csrr/wPsMacSch.c+6681 | wnSend25SlotType | the index of current device in the cmGCb.deviceIdLst:[0]
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[2026-04-29 09:37:42.301840[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop0, slotIdx0, slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301847[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop1, slotIdx1, slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301854[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop2, slotIdx2, slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301861[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop3, slotIdx3, slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301868[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop4, slotIdx4, slotType6,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301875[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop5, slotIdx5, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301882[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop6, slotIdx6, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301889[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop7, slotIdx7, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301896[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop8, slotIdx8, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-29 09:37:42.301903[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6864 | wnSend25SlotType | loop9, slotIdx9, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
问题2的log打印：
[2026-04-30 02:39:39.434366[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6671 | wnSend25SlotType |
dlSchedTm.sfn:[0],dlSchedTm.slot:[0],cmGCb.airNetStatus:40,cmGCb.numDevice3
[2026-04-30 02:39:39.434374[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6672 | wnSend25SlotType | u8CurBcTrbIndex:[0],u8CurIndex:[0],u8LocalIndex:[1]
[2026-04-30 02:39:39.434380[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6681 | wnSend25SlotType | the index of current device in the cmGCb.devicelDlst:[0]
[2026-04-30 02:39:39.434388[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop0, pdschSlot20475, u16CurSlotIndex0,pdschSlotDevid1
,u16DevId1 cmGCb.numDevice3
[2026-04-30 02:39:39.434396[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop1, pdschSlot20476, u16CurSlotIndex1,pdschSlotDevid1
,u16DevId1 cmGCb.numDevice3
[2026-04-30 02:39:39.434404[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop2, pdschSlot20477, u16CurSlotIndex2,pdschSlotDevid1
,u16DevId1 cmGCb.numDevice3
[2026-04-30 02:39:39.434412[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop3, pdschSlot20478, u16CurSlotIndex3,pdschSlotDevid1
,u16DevId1 cmGCb.numDevice3
[2026-04-30 02:39:39.434420[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop4, pdschSlot20479, u16CurSlotIndex4,pdschSlotDevid1
,u16DevId1 cmGCb.numDevice3
[2026-04-30 02:39:39.434427[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop5, pdschSlot20480, u16CurSlotIndex5,pdschSlotDevid2
,u16DevId1 cmGCb.numDevice3
[2026-04-30 02:39:39.434435[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop6, pdschSlot1, u16CurSlotIndex6,pdschSlotDevid1,u16DevId1
cmGCb.numDevice3
[2026-04-30 02:39:39.434442[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop7, pdschSlot2, u16CurSlotIndex7,pdschSlotDevid1,u16DevId1
cmGCb.numDevice3
[2026-04-30 02:39:39.434449[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop8, pdschSlot3, u16CurSlotIndex8,pdschSlotDevid4,u16DevId4
cmGCb.numDevice3
[2026-04-30 02:39:39.434457[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6833 | wnSend25SlotType | loop9, pdschSlot4, u16CurSlotIndex9,pdschSlotDevid1,u16DevId1
cmGCb.numDevice3
[2026-04-30 02:39:39.434464[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop0, slotIdx0, slotType5,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434472[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop1, slotIdx1, slotType5,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434479[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop2, slotIdx2, slotType5,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434486[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop3, slotIdx3, slotType5,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434494[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop4, slotIdx4, slotType5,PreHalf_devid3,PostHalf_devid3
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434503[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop5, slotIdx5, slotType2,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434511[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop6, slotIdx6, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434518[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop7, slotIdx7, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434526[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop8, slotIdx8, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 02:39:39.434534[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6868 | wnSend25SlotType | loop9, slotIdx9, slotType7,PreHalf_devid2,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
解决问题后的log打印：
[2026-04-30 03:03:12.539101[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6671 | wnSend25SlotType |
dlSchedTm.sfn:[1023],dlSchedTm.slot:[10],cmGCb.airNetStatus:40,cmGCb.numDevice3
[2026-04-30 03:03:12.539110[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6672 | wnSend25SlotType | u8CurBcTrbIndex:[31],u8CurIndex:[0],u8LocalIndex:[1]
[2026-04-30 03:03:12.539116[DEBUG]] src/l2/mac/csric/wnPsmacSch.c+6681 | wnSend25SlotType | the index of current device in the cmGCb.devicelDlst:[0]
[2026-04-30 03:03:12.539123[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6867 | wnSend25SlotType | loop0, slotIdx0, slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 03:03:12.539131[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6867 | wnSend25SlotType | loop1, slotIdx1, slotType2,PreHalf_devid3,PostHalf_devid2
u16CurDevIdx0 cmGCb.numDevice3
[2026-04-30 03:03:12.539138[INFO ]] src/l2/mac/csric/wnPsmacSch.c+6867 | wnSend25SlotType | loop2, slotIdx2, slotType2,PreHalf_devid3,PostHalf_devid2

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u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539145[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>3</sup>, slotIdx<sup>3</sup>, slotType<sup>2</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539152[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>4</sup>, slotIdx<sup>4</sup>, slotType<sup>2</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539159[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>5</sup>, slotIdx<sup>5</sup>, slotType<sup>6</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539166[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>6</sup>, slotIdx<sup>6</sup>, slotType<sup>6</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539173[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>7</sup>, slotIdx<sup>7</sup>, slotType<sup>6</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539180[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>8</sup>, slotIdx<sup>8</sup>, slotType<sup>6</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539186[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>9</sup>, slotIdx<sup>9</sup>, slotType<sup>6</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.539602[DEBUG]] src/l2/clms/csrrc/ys\_fapi\_task.c+355 | ysDlm\_FAPI\_FillIDISubfrmMsg | msg\_transfer\_send\_msg success,C\_PLANE  
[2026-04-30 03:03:12.539613[DEBUG]] src/l2/clms/csrrc/ys\_fapi\_task.c+513 | ysDlm\_FAPI\_FillIDICell1Msg | FAPI\_msg,U[0xc2]  
[2026-04-30 03:03:12.539620[DEBUG]] src/l2/clms/csrrc/ys\_fapi\_task.c+554 | ysDlm\_FAPI\_FillIDICell1Msg | msg\_transfer\_send\_msg success,C\_PLANE  
[2026-04-30 03:03:12.544102[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6671 | wnSend25SlotType |  
dlSchedTm.sfn:[0],dlSchedTm.slot:[0],cmGcb.airNetStatus:40,cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544111[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6672 | wnSend25SlotType | u8CurBcTrbIndex:[0],u8CurIndex:[0],u8LocalIndex:[1]  
[2026-04-30 03:03:12.544117[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6681 | wnSend25SlotType | the index of current device in the cmGcb.devicelst :[0]  
[2026-04-30 03:03:12.544125[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>0</sup>, pdschSlot<sup>20475</sup>, u16CurSlotIndex<sup>0</sup>,pdschSlotDevicel<sup>1</sup>  
,u16DevId<sup>1</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544133[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>1</sup>, pdschSlot<sup>20476</sup>, u16CurSlotIndex<sup>1</sup>,pdschSlotDevicel<sup>1</sup>  
,u16DevId<sup>1</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544140[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>2</sup>, pdschSlot<sup>20477</sup>, u16CurSlotIndex<sup>2</sup>,pdschSlotDevicel<sup>1</sup>  
,u16DevId<sup>1</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544147[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>3</sup>, pdschSlot<sup>20478</sup>, u16CurSlotIndex<sup>3</sup>,pdschSlotDevicel<sup>1</sup>  
,u16DevId<sup>1</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544154[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>4</sup>, pdschSlot<sup>20479</sup>, u16CurSlotIndex<sup>4</sup>,pdschSlotDevicel<sup>1</sup>  
,u16DevId<sup>1</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544162[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>5</sup>, pdschSlot<sup>9</sup>, u16CurSlotIndex<sup>5</sup>,pdschSlotDevicel<sup>1</sup>,u16DevId<sup>1</sup>  
cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544168[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>6</sup>, pdschSlot<sup>1</sup>, u16CurSlotIndex<sup>6</sup>,pdschSlotDevicel<sup>1</sup>,u16DevId<sup>1</sup>  
cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544176[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>7</sup>, pdschSlot<sup>2</sup>, u16CurSlotIndex<sup>7</sup>,pdschSlotDevicel<sup>1</sup>,u16DevId<sup>1</sup>  
cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544183[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>8</sup>, pdschSlot<sup>3</sup>, u16CurSlotIndex<sup>8</sup>,pdschSlotDevicel<sup>1</sup>,u16DevId<sup>1</sup>  
cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544190[DEBUG]] src/l2/mac/csrrc/wnPsmacSch.c+6832 | wnSend25SlotType | loop<sup>9</sup>, pdschSlot<sup>4</sup>, u16CurSlotIndex<sup>9</sup>,pdschSlotDevicel<sup>1</sup>,u16DevId<sup>1</sup>  
cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544197[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>0</sup>, slotIdx<sup>0</sup>, slotType<sup>5</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544204[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>1</sup>, slotIdx<sup>1</sup>, slotType<sup>5</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544211[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>2</sup>, slotIdx<sup>2</sup>, slotType<sup>5</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544219[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>3</sup>, slotIdx<sup>3</sup>, slotType<sup>5</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544226[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>4</sup>, slotIdx<sup>4</sup>, slotType<sup>5</sup>,PreHalf\_devid<sup>3</sup>,PostHalf\_devid<sup>3</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544233[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>5</sup>, slotIdx<sup>5</sup>, slotType<sup>7</sup>,PreHalf\_devid<sup>2</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544240[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>6</sup>, slotIdx<sup>6</sup>, slotType<sup>7</sup>,PreHalf\_devid<sup>2</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544247[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>7</sup>, slotIdx<sup>7</sup>, slotType<sup>7</sup>,PreHalf\_devid<sup>2</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544253[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>8</sup>, slotIdx<sup>8</sup>, slotType<sup>7</sup>,PreHalf\_devid<sup>2</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>  
[2026-04-30 03:03:12.544260[INFO ]] src/l2/mac/csrrc/wnPsmacSch.c+6867 | wnSend25SlotType | loop<sup>9</sup>, slotIdx<sup>9</sup>, slotType<sup>7</sup>,PreHalf\_devid<sup>2</sup>,PostHalf\_devid<sup>2</sup>  
u16CurDevIdx<sup>0</sup> cmGcb.numDevice<sup>3</sup>