### STE - 错误 #668

## FPGA使用FH进行demo测试,概率性出现跑一会接收异常

2021-10-19 20:42 - 杨晋

状态: 开始日期: 挂起 2021-10-19 优先级: 普通 计划完成日期: 指派给: 杨晋 % 完成: 0% 类别: 预期时间: 0.00 小时 目标版本: 耗时: 0.00 小时 问题归属: CPU类型:

#### 描述

```
Demo start running:

tx0:1, rx0:2 symbol_max num:14 symbol_size_max:65536

[Rx] Tput=6122Mbps timeDiff=4800180us rxTotal=3673600000 txCount=10000 rxCount=1, tx_seq: 10000, rx_seq: 10001, diff: 2, ttiCount: 10000

[Rx] Tput=6122Mbps timeDiff=480068sus txTotal=3673600000 txCount=10000 rxCount=1, tx_seq: 10000, rx_seq: 10001, diff: 1, ttiCount: 10001

[Rx] Tput=6122Mbps timeDiff=4800068sus txTotal=3673600000 txCount=9998 rxCount=10000, tx_seq: 19998, rx_seq: 20000, diff: 2, ttiCount: 20000

[Rx] Tput=6122Mbps timeDiff=4800135us rxTotal=3673600000 txCount=9998 rxCount=10000, tx_seq: 29999, rx_seq: 20000, diff: 1, ttiCount: 20001

[Rx] Tput=6122Mbps timeDiff=4800135us rxTotal=3673600000 txCount=9999 rxCount=10000, tx_seq: 29999, rx_seq: 30000, diff: 1, ttiCount: 30001

[Rx] Tput=6122Mbps timeDiff=4800035us rxTotal=3673600000 txCount=9999 rxCount=10000, tx_seq: 39999, rx_seq: 40000, diff: 1, ttiCount: 30001

[Rx] Tput=6122Mbps timeDiff=4800035us rxTotal=3673600000 txCount=9999 rxCount=10000, tx_seq: 39999, rx_seq: 40000, diff: 0, ttiCount: 40001

[Rx] Tput=6122Mbps timeDiff=48000035us rxTotal=3673600000 txCount=9999 rxCount=10000, tx_seq: 39999, rx_seq: 40000, diff: 0, ttiCount: 40001

[Rx] Tput=6122Mbps timeDiff=48000035us rxTotal=3673600000 txCount=9999 rxCount=10000, tx_seq: 39999, rx_seq: 50000, diff: 0, ttiCount: 50000

[Rx] Tput=6122Mbps timeDiff=4800137us rxTotal=3673600000 txCount=9999 rxCount=10000, tx_seq: 59999, rx_seq: 60000, diff: 0, ttiCount: 50000

[Rx] Tput=6122Mbps timeDiff=4800137us rxTotal=3673600000 txCount=10000 rxCount=0, tx_seq: 50000, rx_seq: 60000, diff: 0, ttiCount: 50000

[Rx] Tput=6122Mbps timeDiff=4800004us txTotal=3673600000 txCount=10000 rxCount=0, tx_seq: 50000, rx_seq: 50000, diff: 0, ttiCount: 50000

[Rx] Tput=6122Mbps timeDiff=4800004us txTotal=36736000000 txCount=10000 rxCount=0, tx_seq: 50000, rx_seq: 50000, diff: 0, ttiCount: 50000

[Rx] Tput=6122Mbps timeDiff=4800004us txTotal=36736000000 txCount=100000 tx_seq: 500000, rx_seq: 30000, diff: 0, ttiCount: 50000

[Rx] Tput=6122Mbps
```

#### 现象如上

### 相关的问题:

关联到 BA5000平台 - 错误 #674: fhdemo 绑定cpu后,接收时出现rx\_seq 跟 ttiCo... 已解决 2021-10-26

#### 历史记录

#1 - 2021-10-20 10:13 - guo hanlin

- 项目 从 STE UT 变更为 STE

#2 - 2021-10-20 10:14 - 杨晋

- 主题 从 FPGA 使用FH进行demo测试,跑一会出现接收异常 变更为 FPGA 使用FH进行demo测试,概率性出现跑一会接收异常
- 指派给 被设置为 杨 晋

#3 - 2021-10-20 18:20 - 杨晋

经调测,发现在出现上述接收包变少时,fpga进入dma模块的fft数据包并没有少,且出现无可用描述符的错误标志,说明这个问题与完成标志的上报和dma下发描述符等有关

#4 - 2021-10-22 17:17 - 杨晋

- 文件 rx\_neg1.png 已添加

#5 - 2021-10-22 17:19 - **杨晋** 

- 状态 从 新建 变更为 挂起

新出了加调测和采数版本,但问题一直无法复现,暂时挂起。再全系统环境测试

#6 - 2021-10-26 16:23 - 杨晋

- 关联到 错误#673: fhdemo tti 丢失 已添加

2025-05-07 1/2

## #7 - 2021-10-26 16:23 - 杨晋

- 关联到 错误#674: fhdemo 绑定cpu后,接收时出现rx\_seq 跟 ttiCount 逐渐增大的情况。 已添加

# 文件

rx_dis.png	85.5 KB	2021-10-19	杨晋
rx_neg1.png	39.4 KB	2021-10-22	杨晋

2025-05-07 2/2